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From: Robert D. Marshall, Jr.
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

TI-30481

Gary L. Swoboda, et al.

Art Unit: 2128

Serial No.: 09/943,599

Examiner: Akash Saxena

Filed: August 30, 2001

Conf. No.: 2479

For: Correlating On-Chip Data Processor Trace Information for Export

CERTIFICATION OF FACSIMILE TRANSMISSION

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Robin E. Barnum

June 21, 2005
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FACSIMILE COVER SHEET

<input checked="" type="checkbox"/> FACSIMILE COVER SHEET	<input checked="" type="checkbox"/> AMENDMENT <u>111</u> (25 Pages)
<input type="checkbox"/> NEW APPLICATION	<input type="checkbox"/> EOT (# Pages)
<input type="checkbox"/> DECLARATION (# Pages)	<input type="checkbox"/> NOTICE OF APPEAL (# Pages)
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<input type="checkbox"/> CONTINUATION APP'N (# Pages)	
<input type="checkbox"/> DIVISIONAL APP'N	
NAME OF INVENTOR(S): Gary L. Swoboda, et al.	
RECEIPT DATE & SERIAL NO.: Serial No.: 09/943,599 Filing Date: August 30, 2001	
TITLE OF INVENTION: Correlating On-Chip Data Processor Trace Information for Export	
TI FILE NO.: TI-30481	DEPOSIT ACCT. NO.: 20-0668
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Swoboda et al Art Unit: 2128
Serial No.: 09/943,599 Examiner: Akash Saxena
Filed: August 30, 2001 Docket: TI-30481
For: CORRELATING ON-CHIP DATA PROCESSOR TRACE INFORMATION FOR
EXPORT

AMENDMENT UNDER 37 C.F.R. 1.111

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Robin E. Barnum

Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated March 22, 2005. These amendments are respectfully submitted as a full and complete response to that Action.

Please amend the above referenced application as follows: